

PG160-B02

8GB GDDR6, 256b, X16

Tall DVI-D + USBC/DP + DP + HDMI/DP + DP

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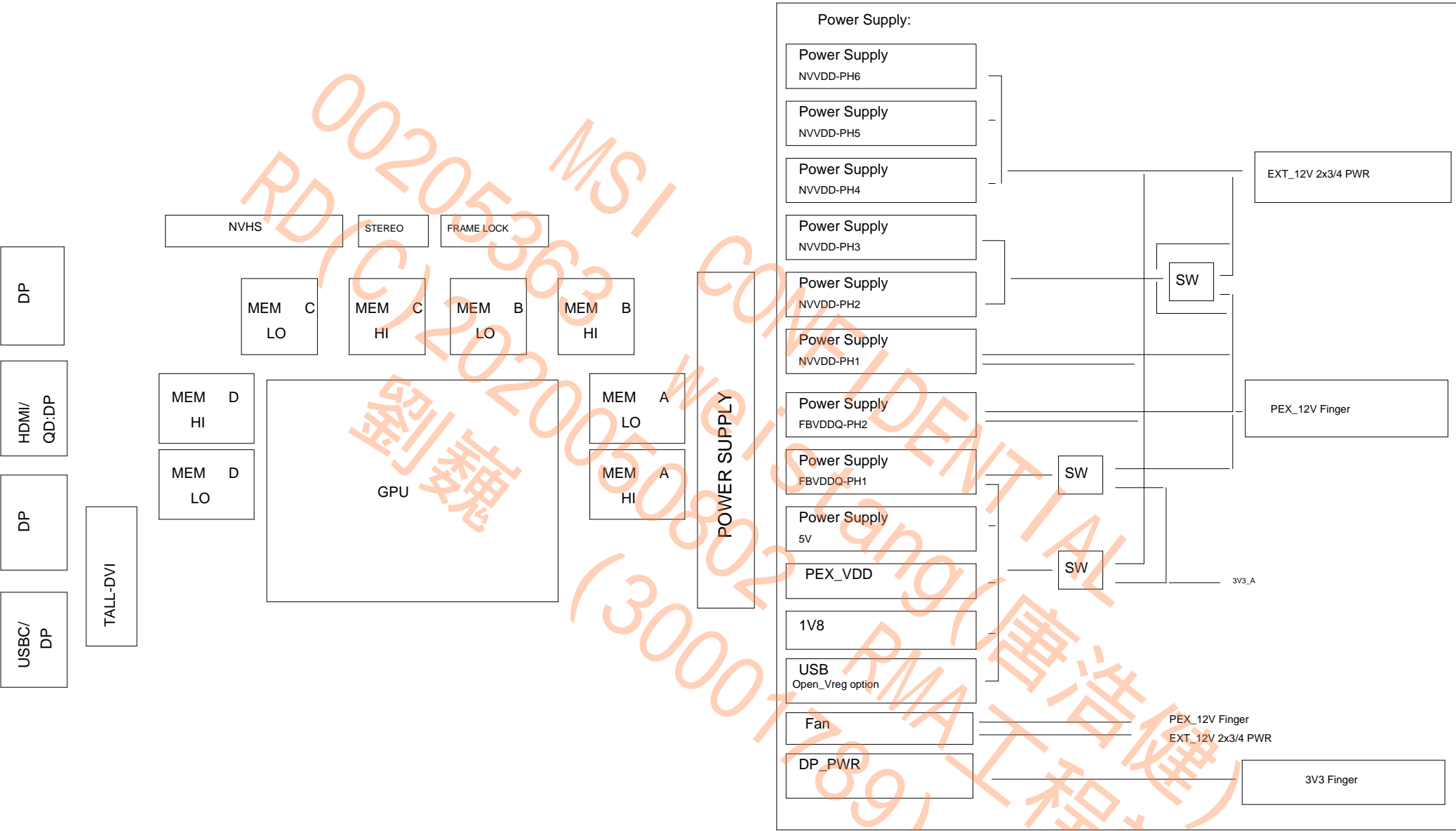
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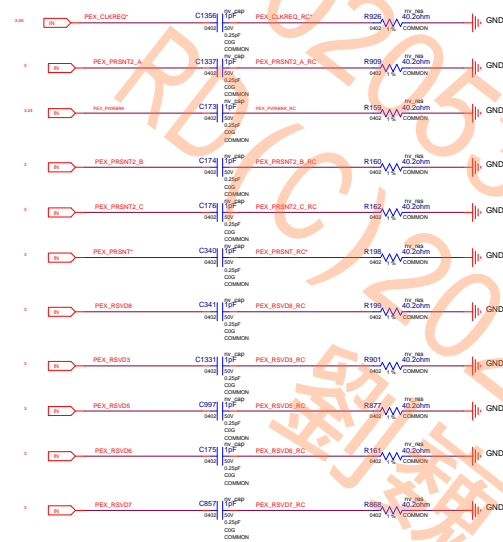


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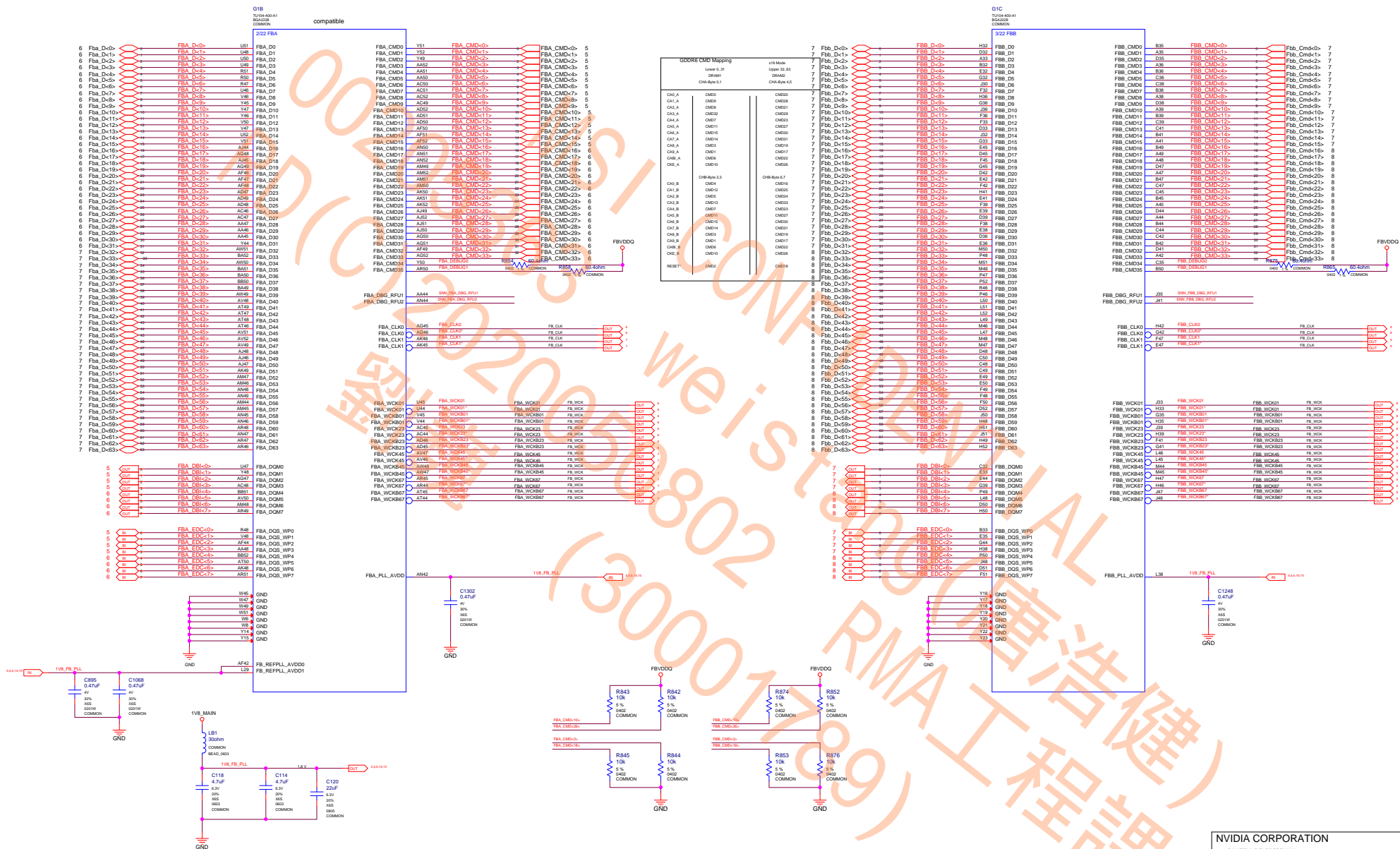


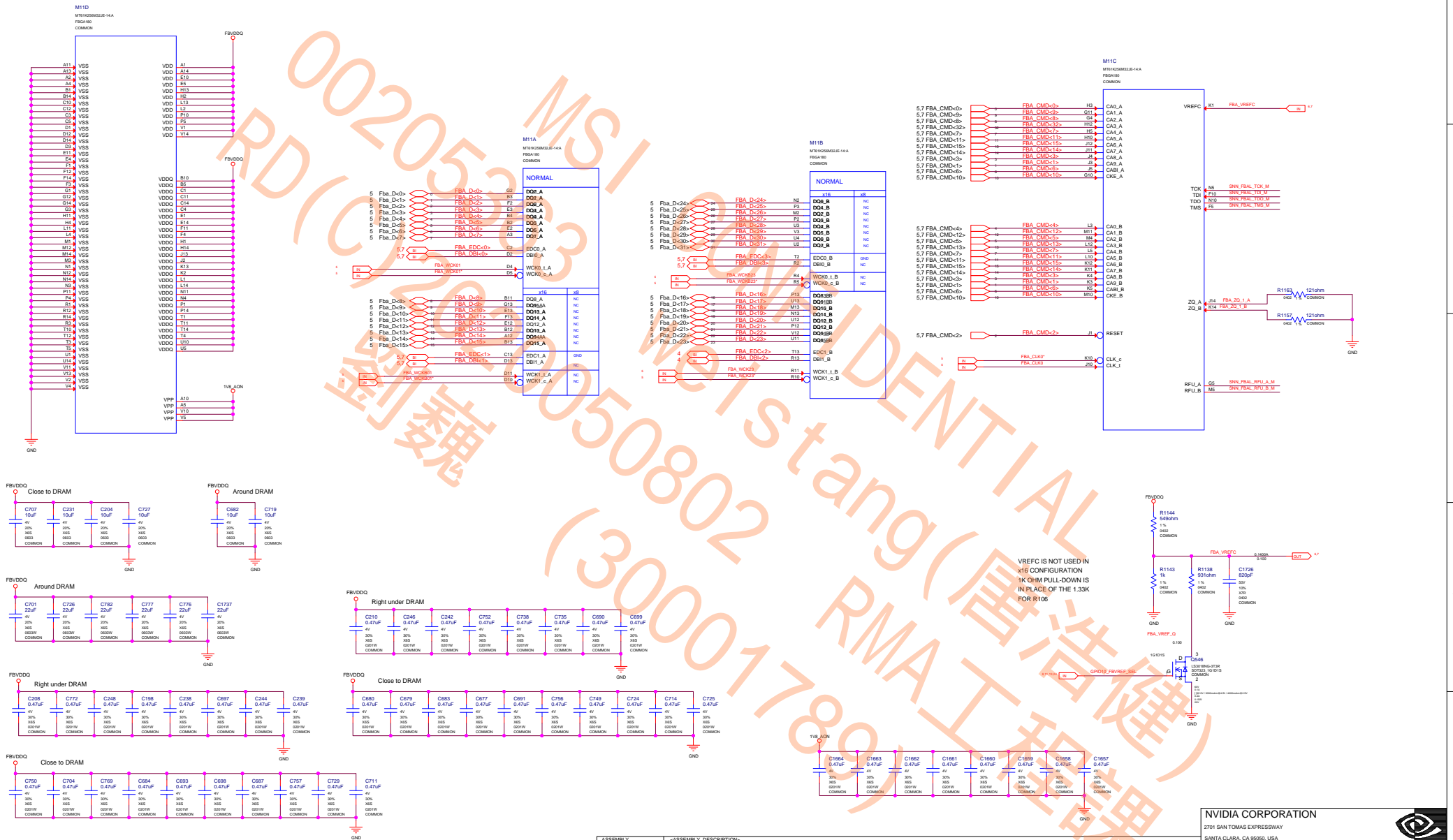
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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PCIE RC TERM

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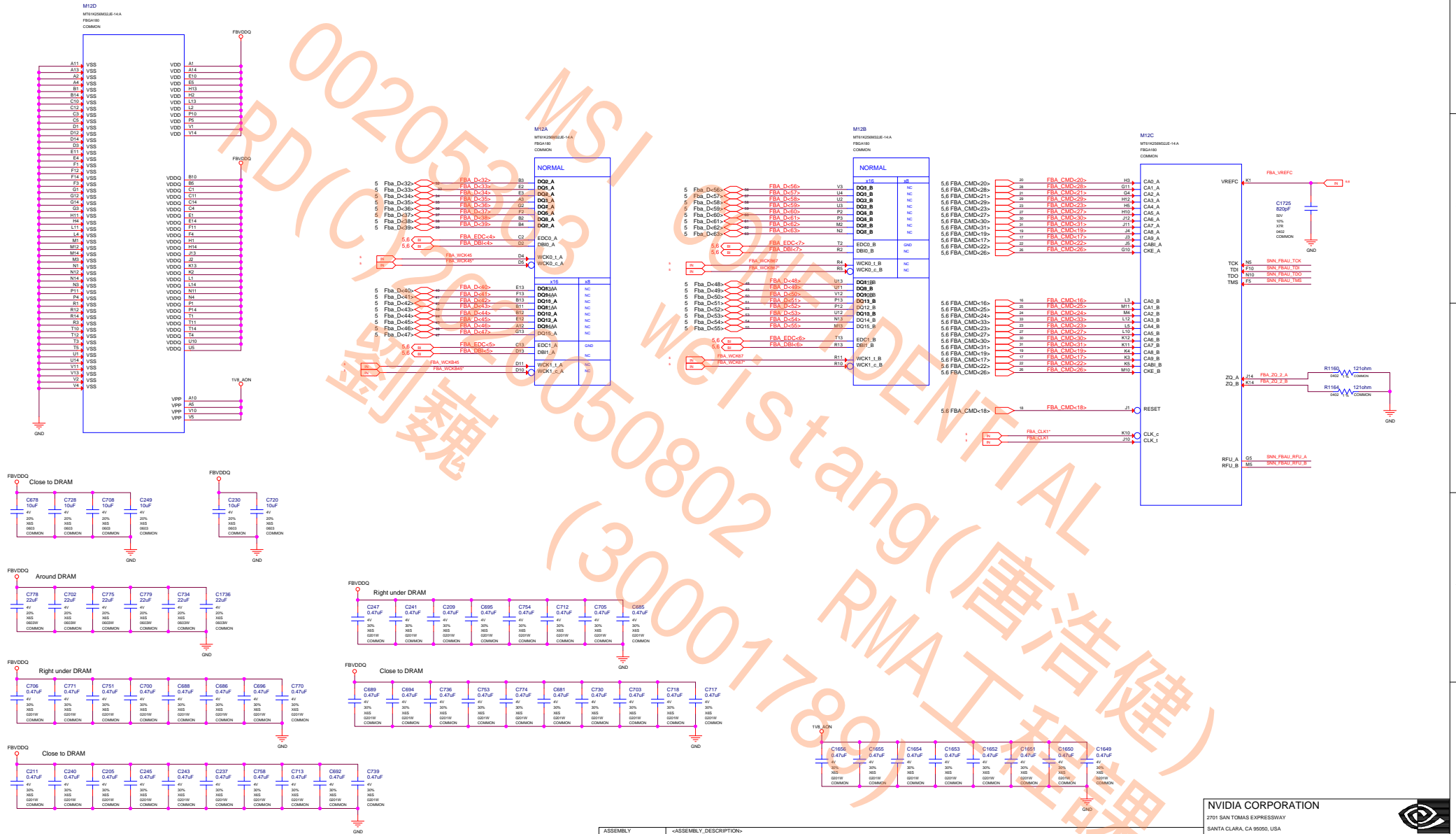


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BCM REV: A
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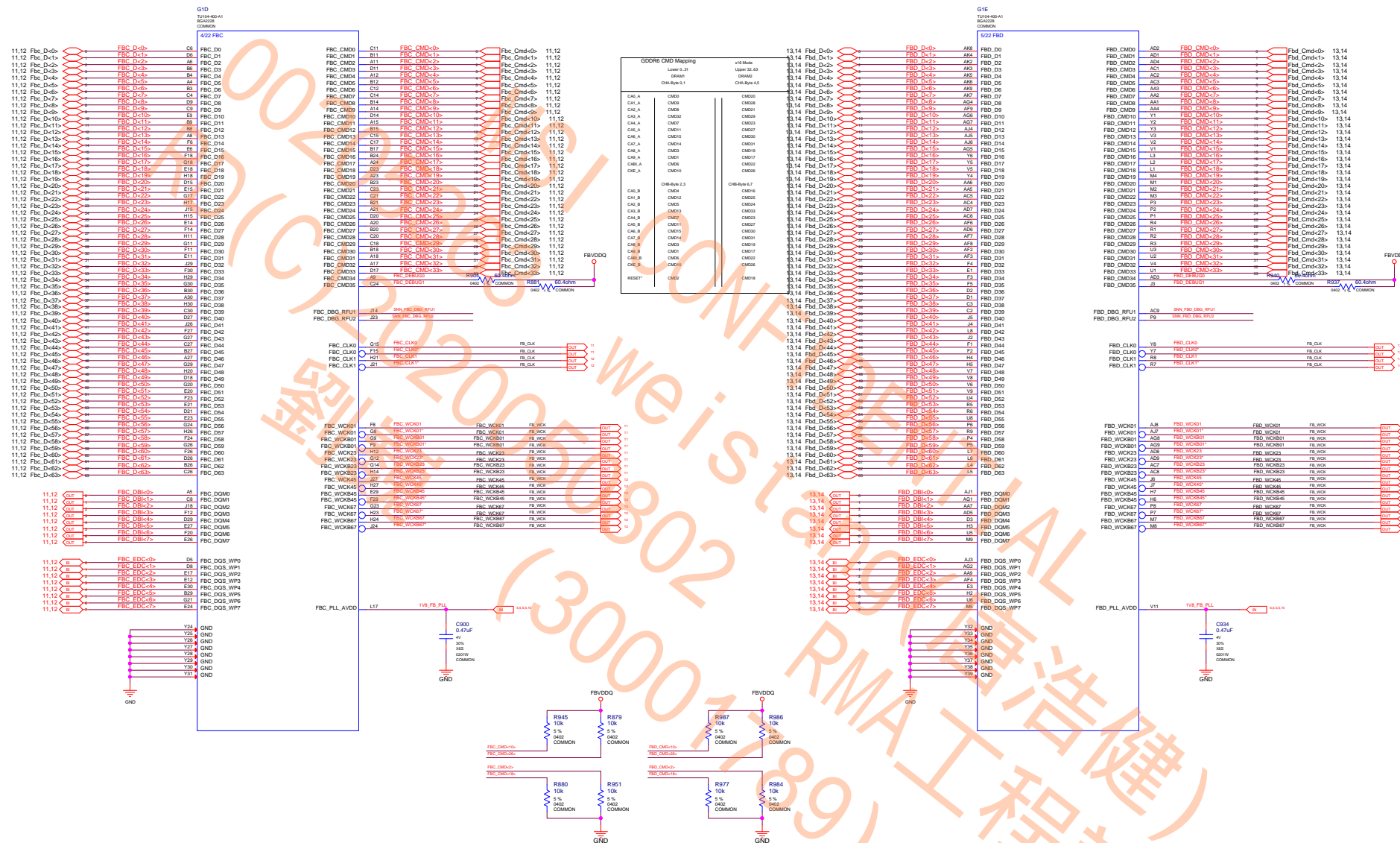






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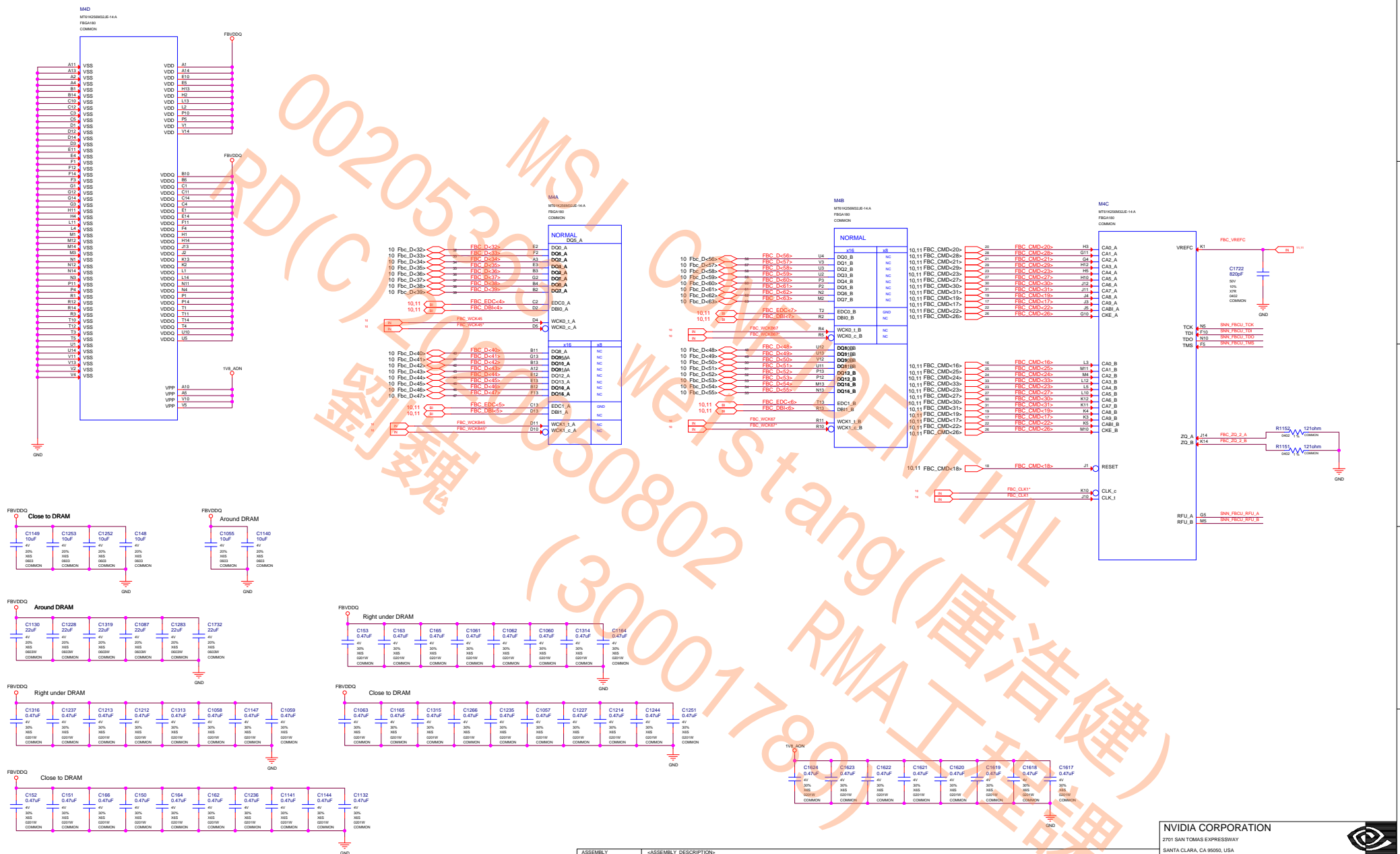
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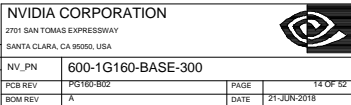
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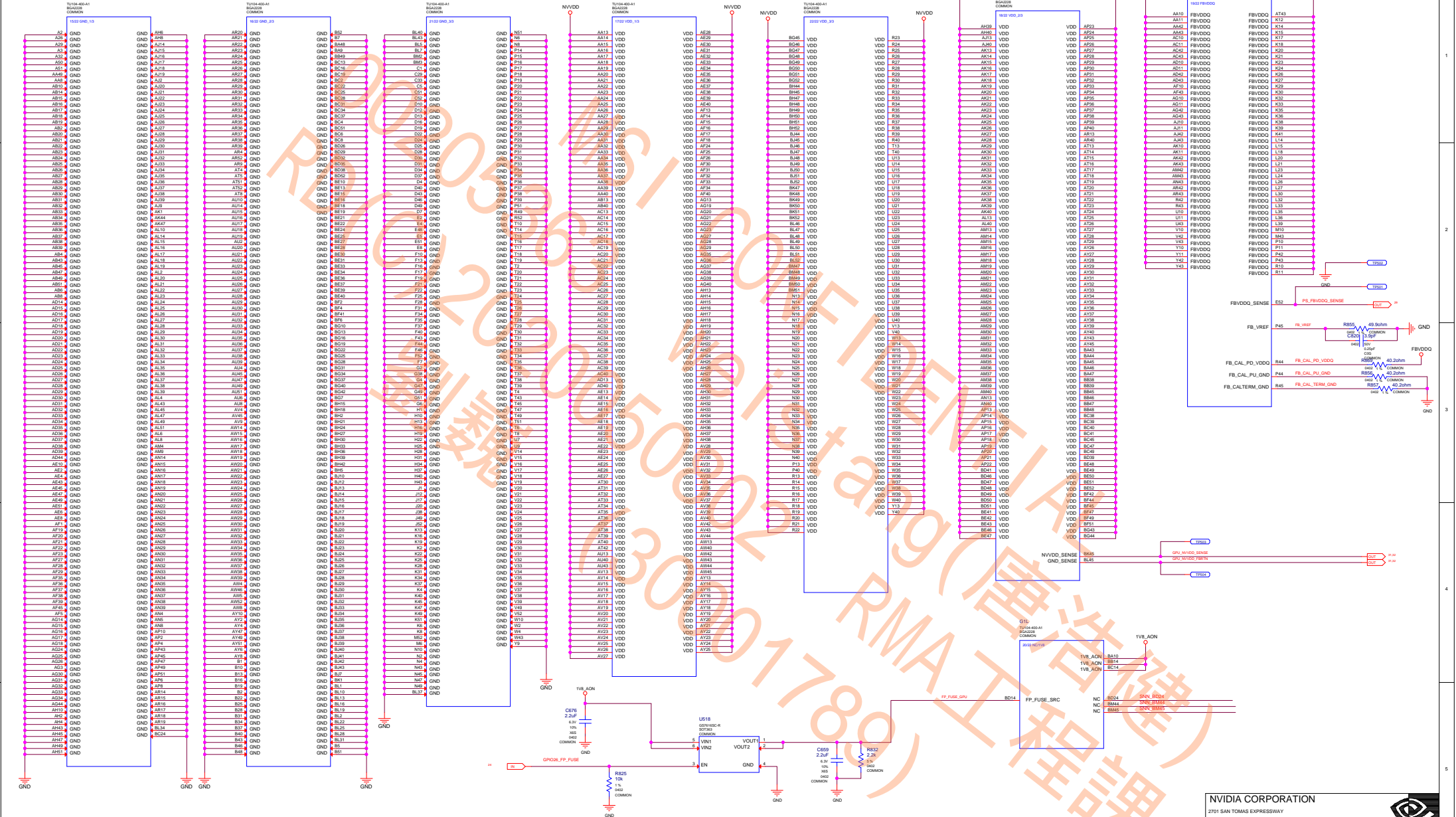




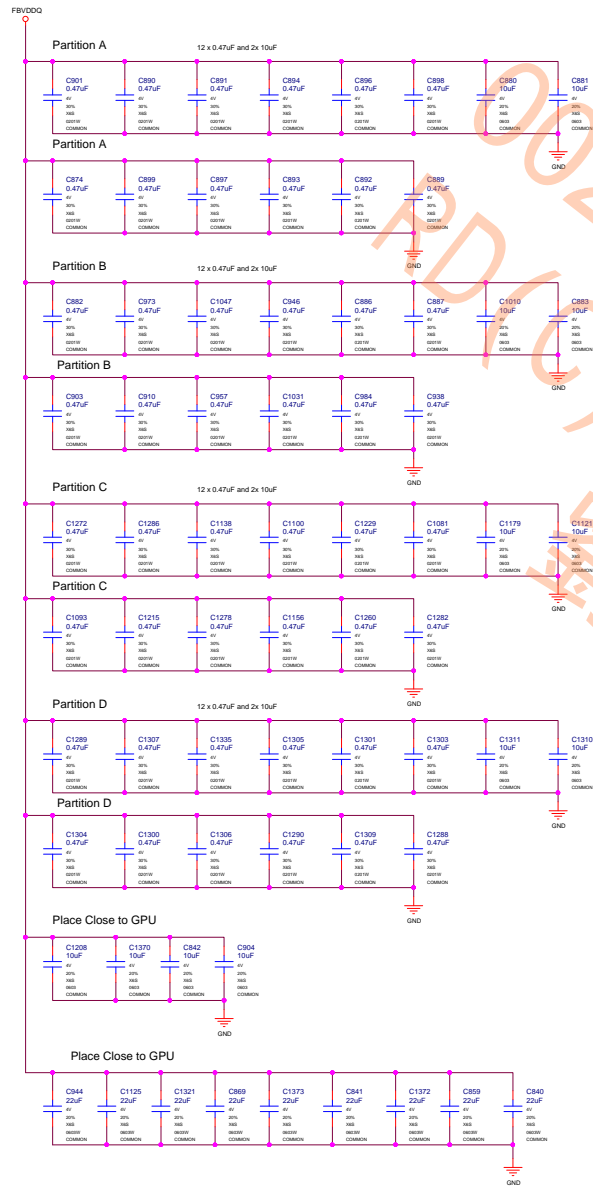
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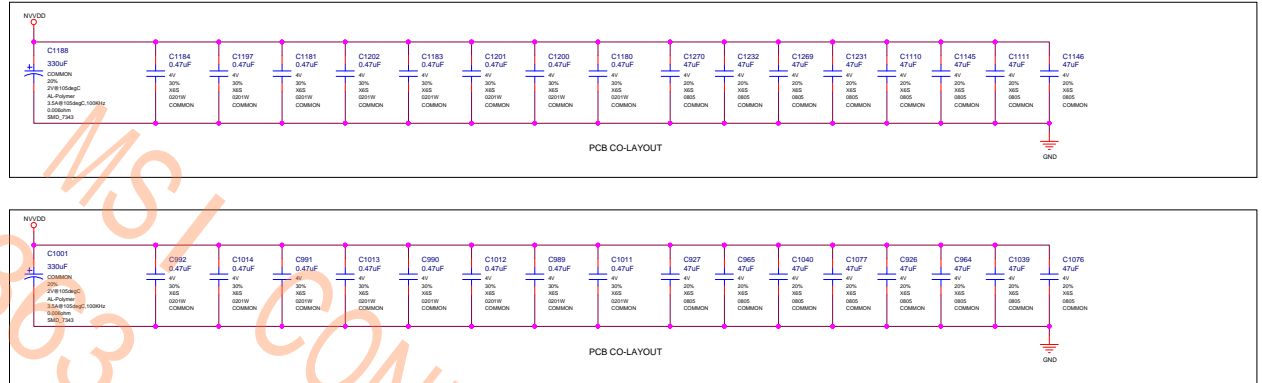




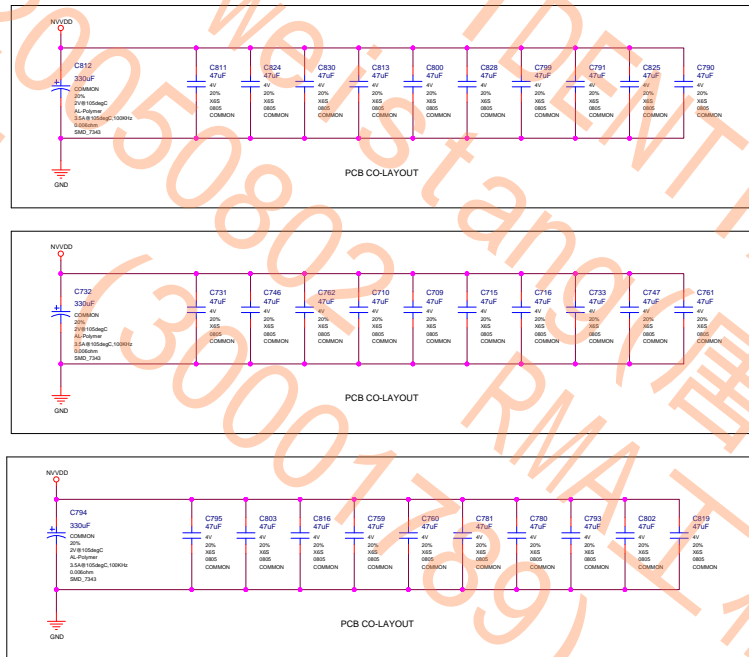
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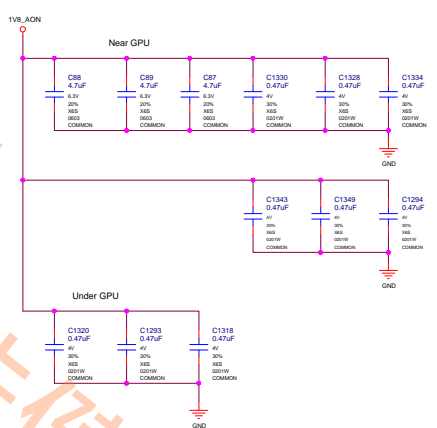
Under GPU



Near GPU



1V8_AON



ASSEMBLY	ASSEMBLY DESCRIPTION
PAGE DETAIL	GPU Decoupling

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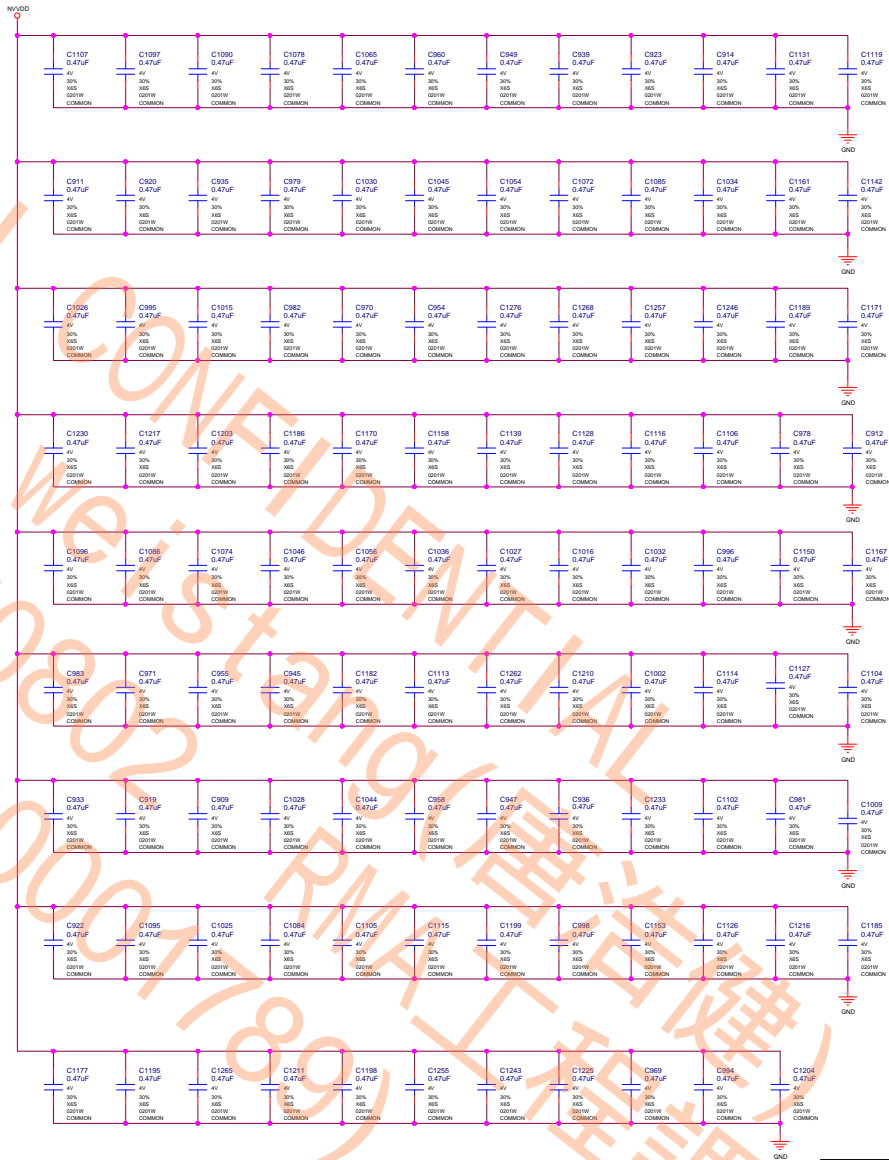


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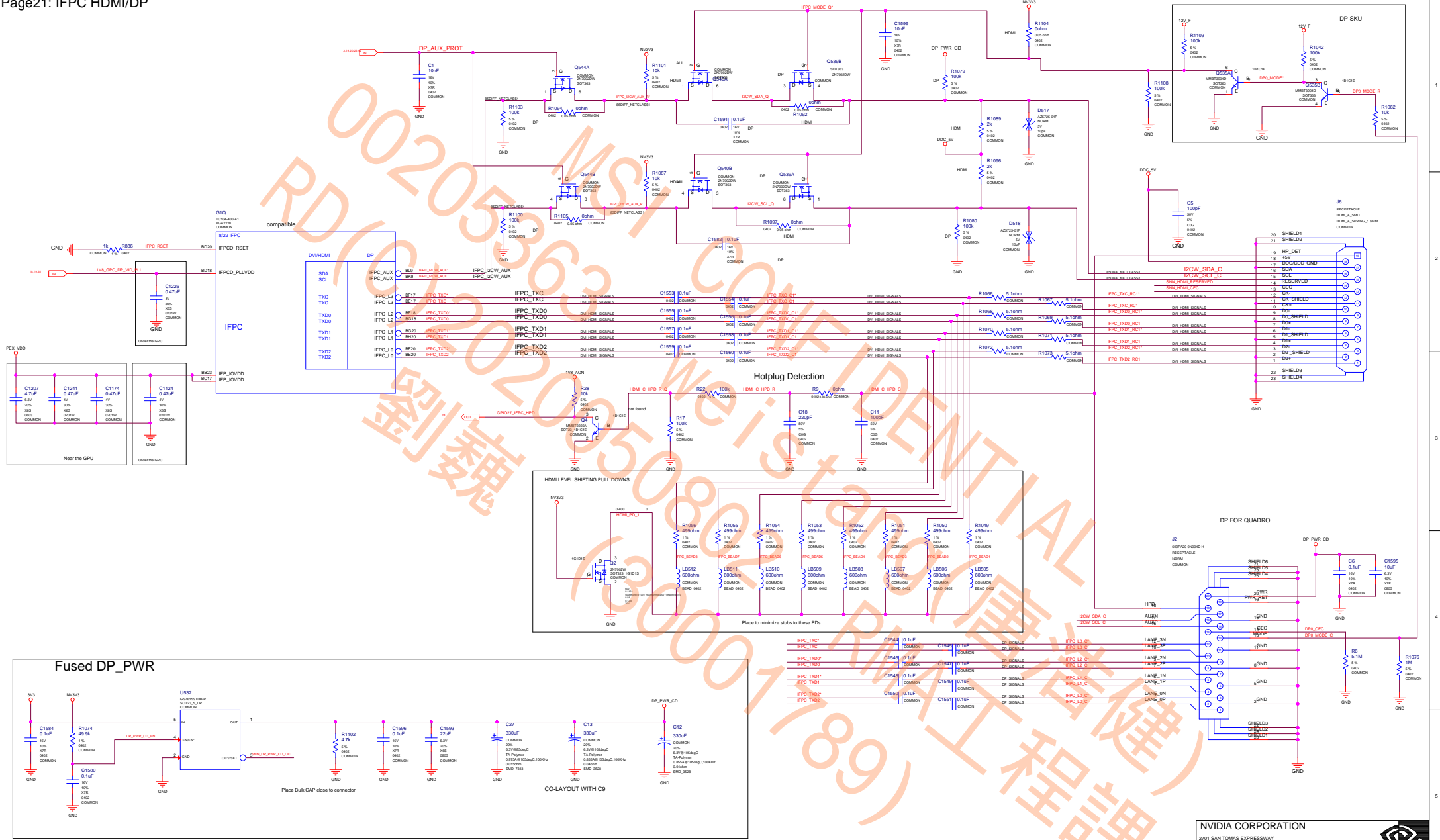
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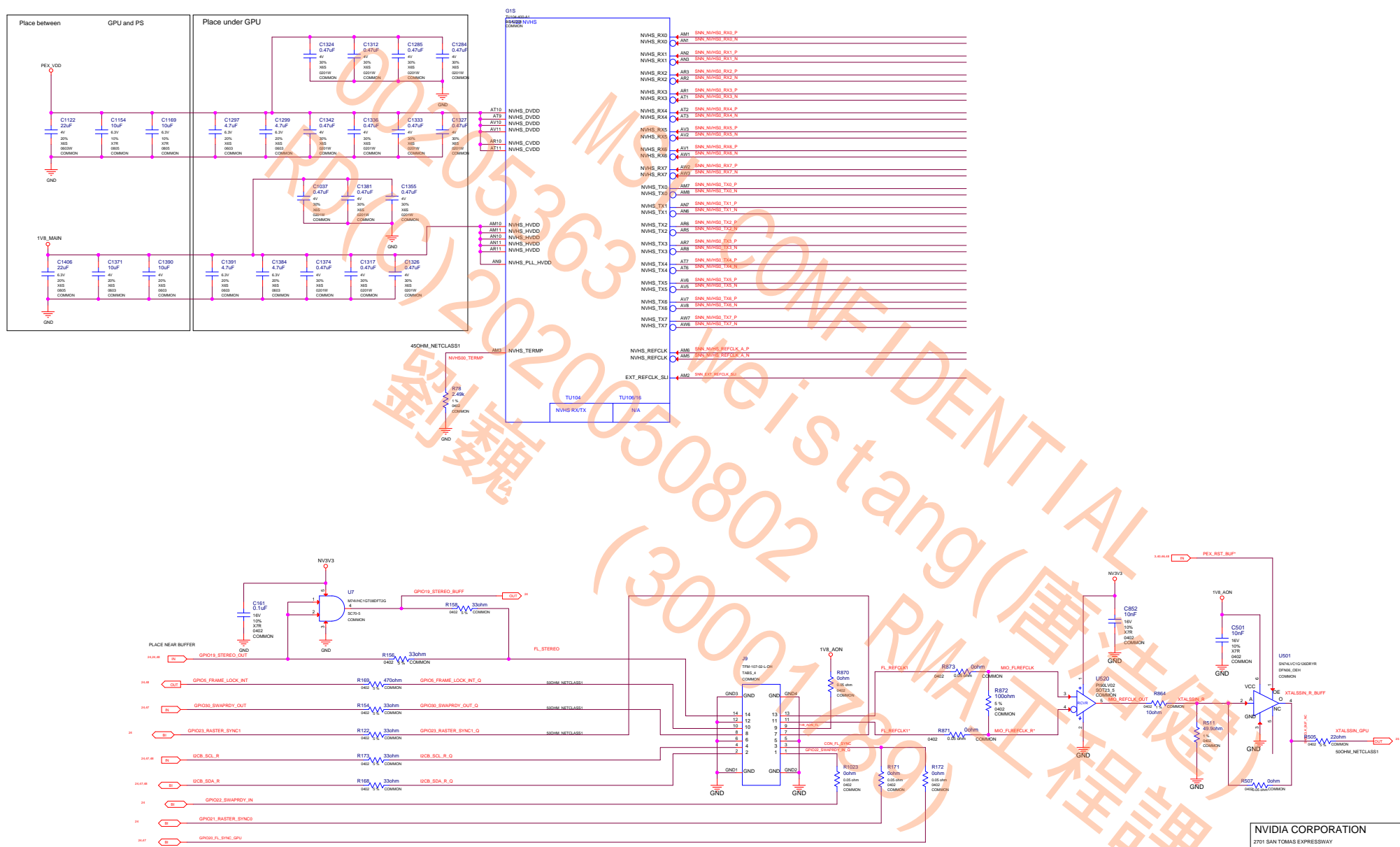
NVDD



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PAGE DETAIL		ID: IFPC HDMI/DP			
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ASSEMBLY	«ASSEMBLY_DESCRIPTION»
PAGE DETAIL	IO: NVHS Interface and Frame Lock

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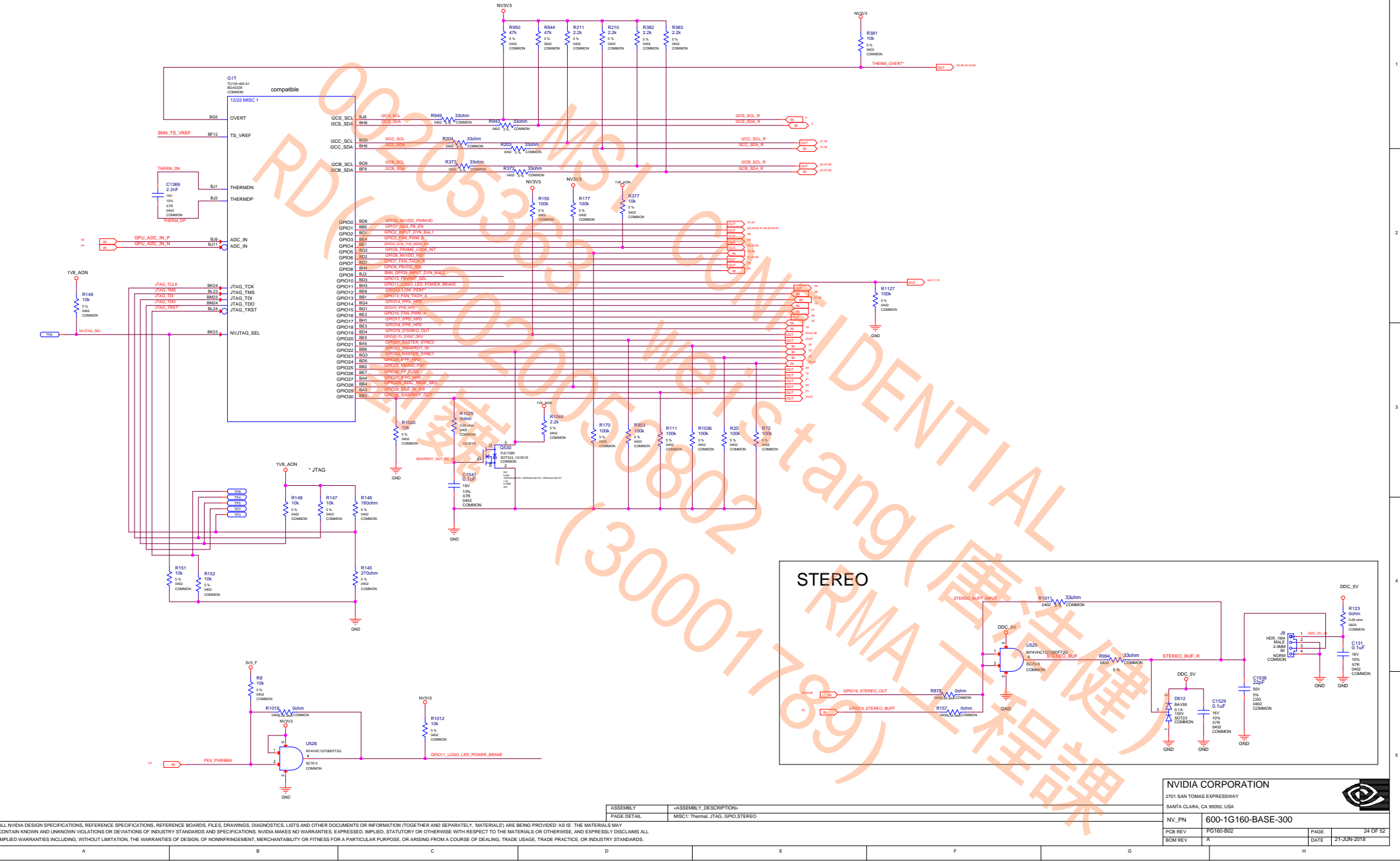
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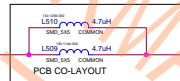


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ASSEMBLY	ASSEMBLY DESCRIPTION
PAGE DETAIL	MISC1: Thermal, JTAG, GPIO, STEREO

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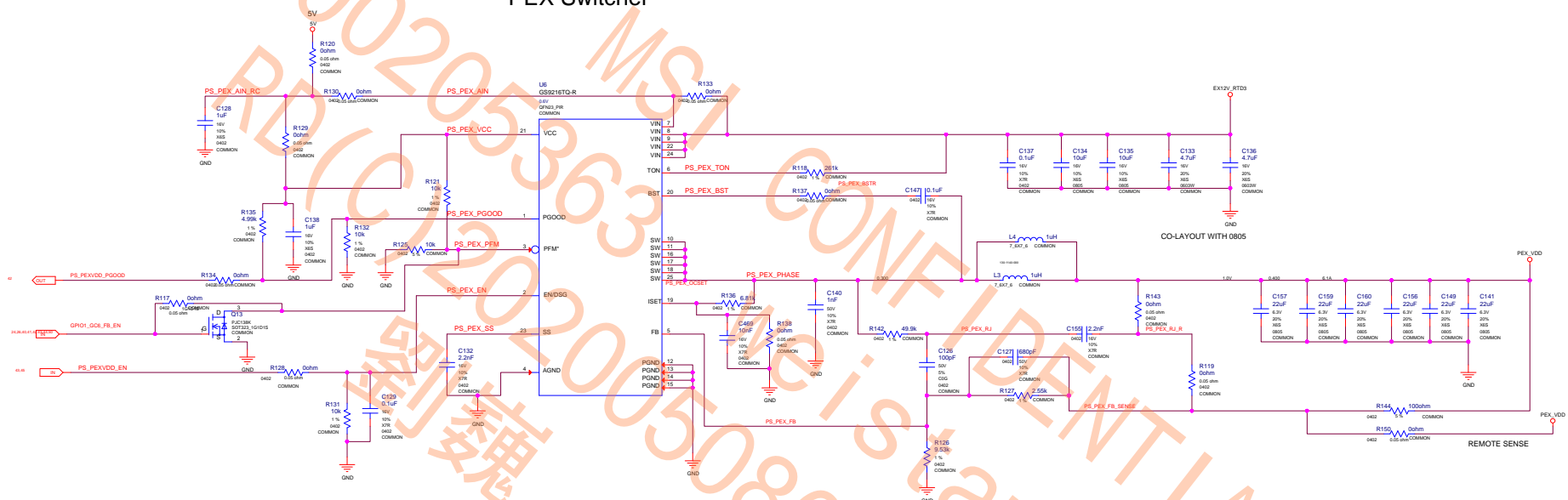


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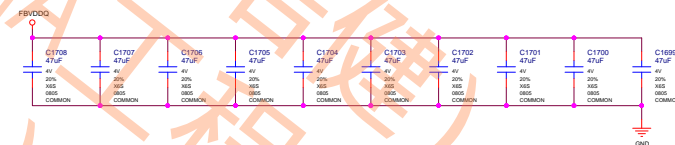
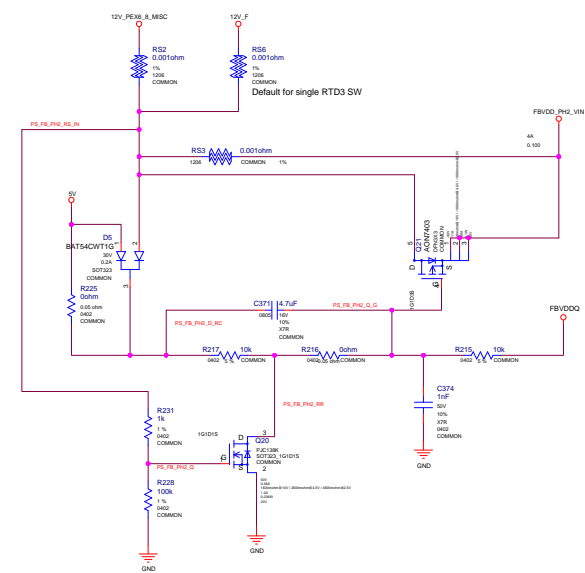
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PEX Switcher

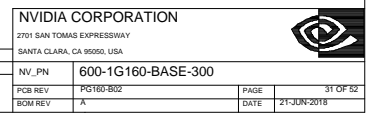


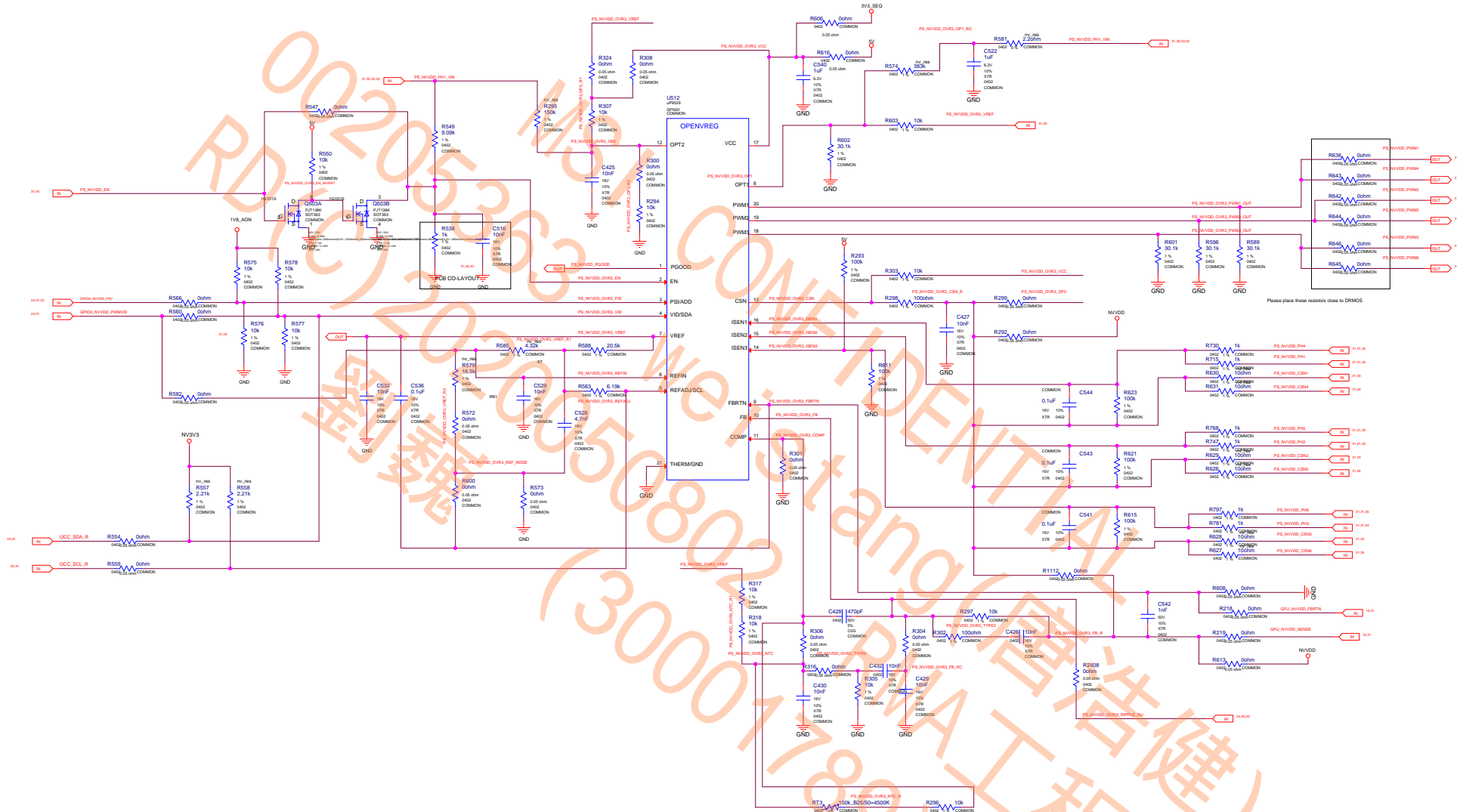
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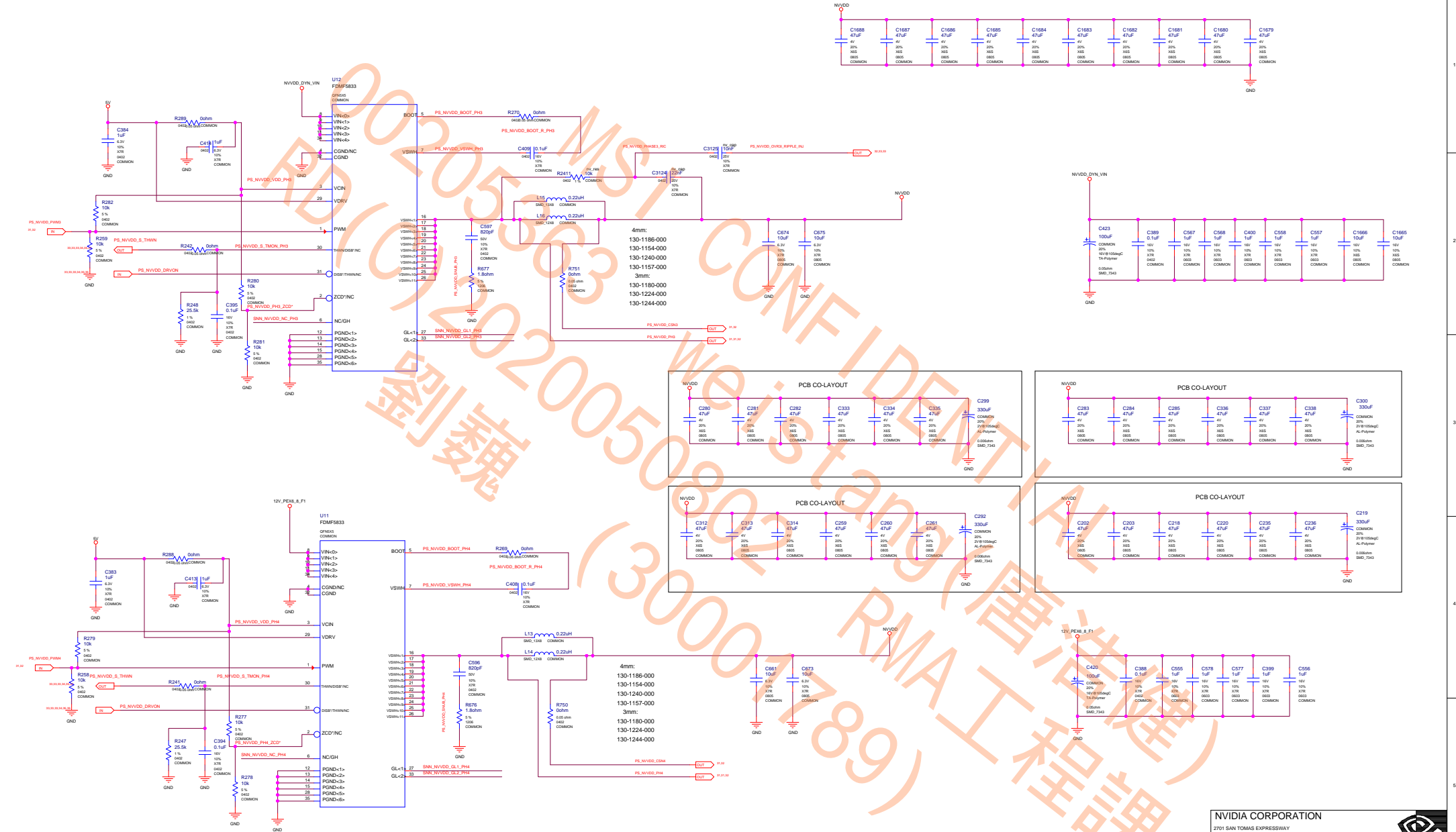






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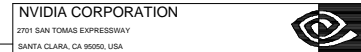
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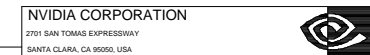


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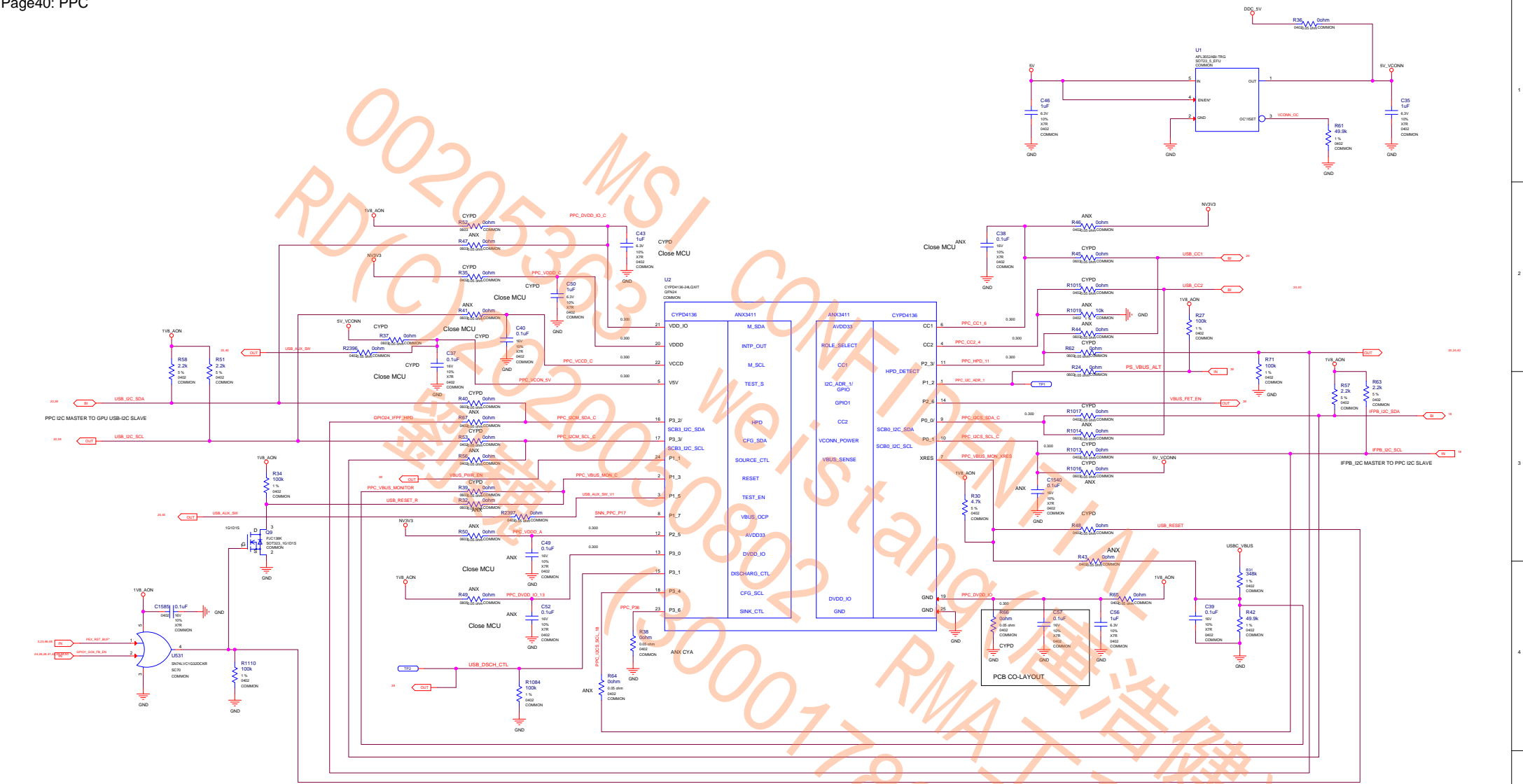
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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PS: Input, Filtering, and Monitoring

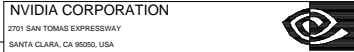
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	SCL	SDA
CYPD I2C SLAVE	PIN 10	PIN 9
CYPD I2C MASTER	PIN 17	PIN 16
ANX I2C SLAVE	PIN 18	PIN 17
ANX I2C MASTER	PIN 22	PIN 21

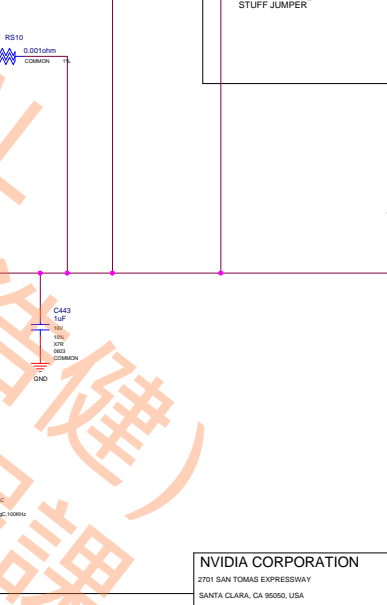
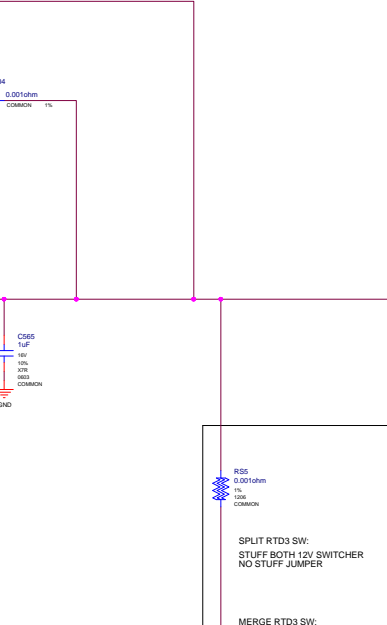
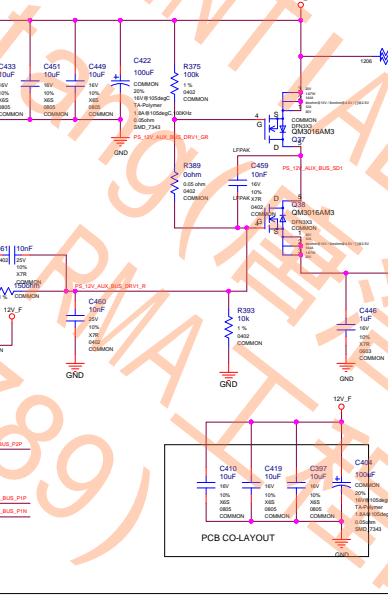
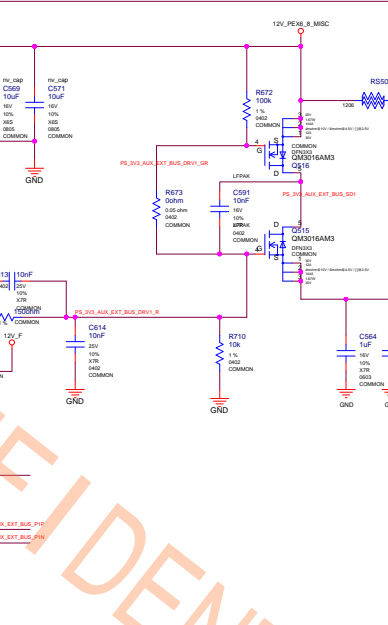
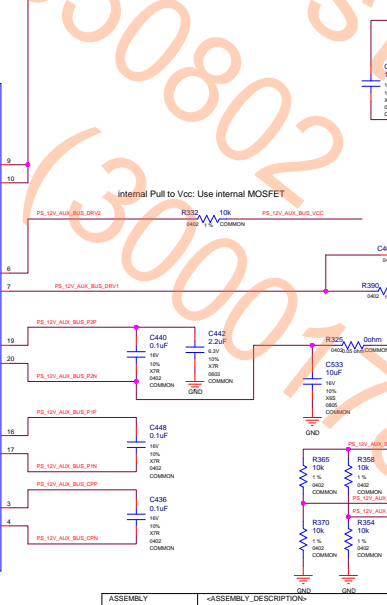
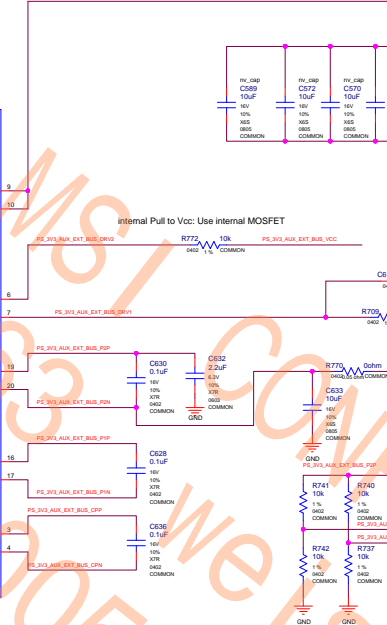
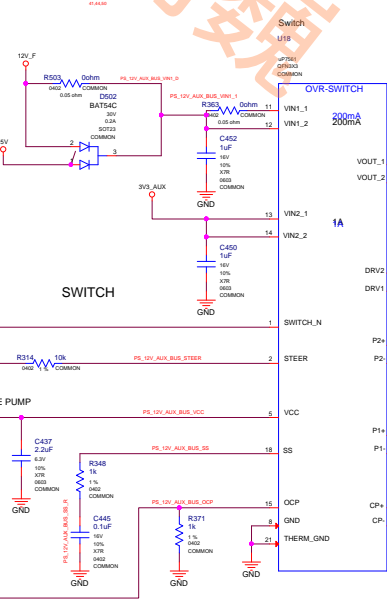
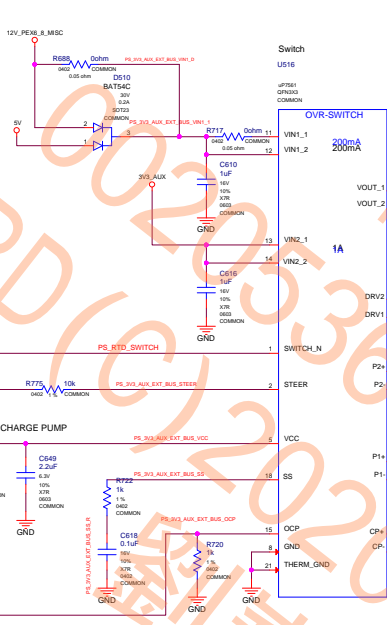
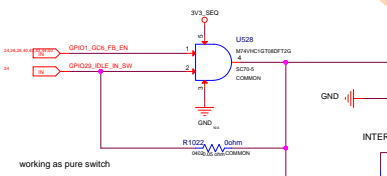
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ASSEMBLY	ASSEMBLY DESCRIPTION
PAGE DETAIL	PD PPC



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AND GATE LOGIC			
GPIO1	GPIO29	SWITCH	VOUT
0	0	0	12V
0	1	0	12V
1	0	0	12V
1	1	1	3V3A



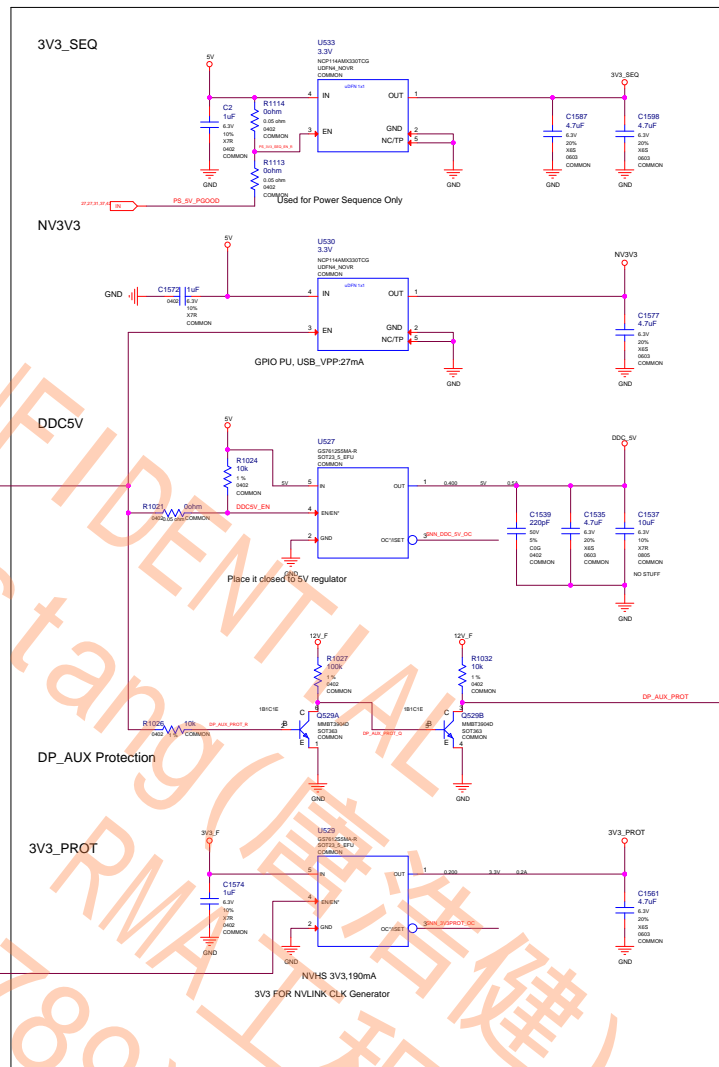
SPLIT RTD3 SW:
STUFF BOTH 12V SWITCHER
NO STUFF JUMPER

MERGE RTD3 SW:
NO STUFF 12V RTD3 SWITCHER
STUFF JUMPER

ASSEMBLY	«ASSEMBLY_DESCRIPTION
PAGE DETAIL	PS: 12V & 3V3_A SWITCHER

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NV_PN		600-1G160-BASE-300		
PCB REV		PG160-B02		PAGE
BOM REV		A		DATE
				41 OF 52 21-JUN-2018

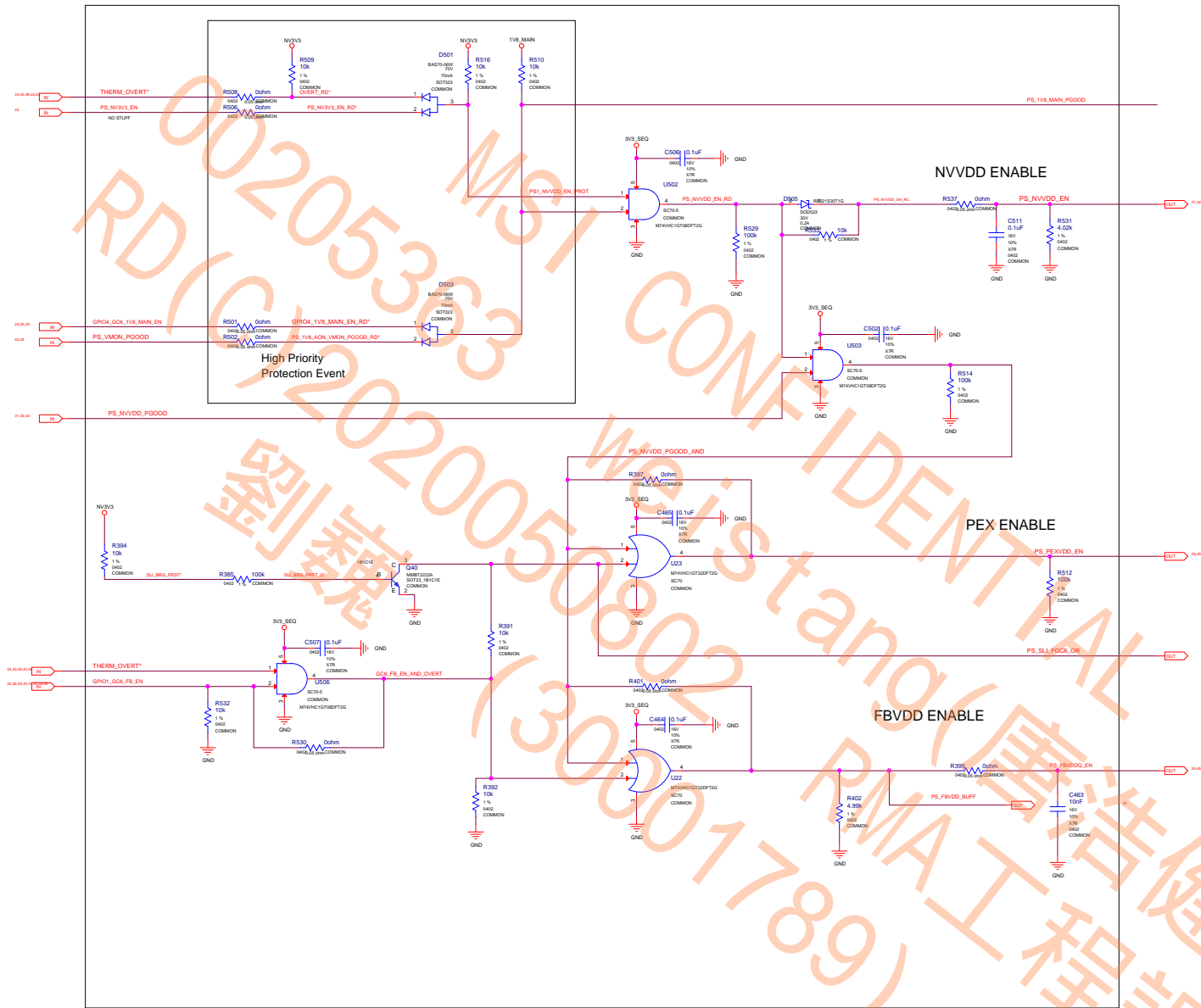




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PAGE DETAIL	SEQ: 5V, 1V8, NV3V3 ENABLE



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BOM REV	A	DATE	21-JUN-2018

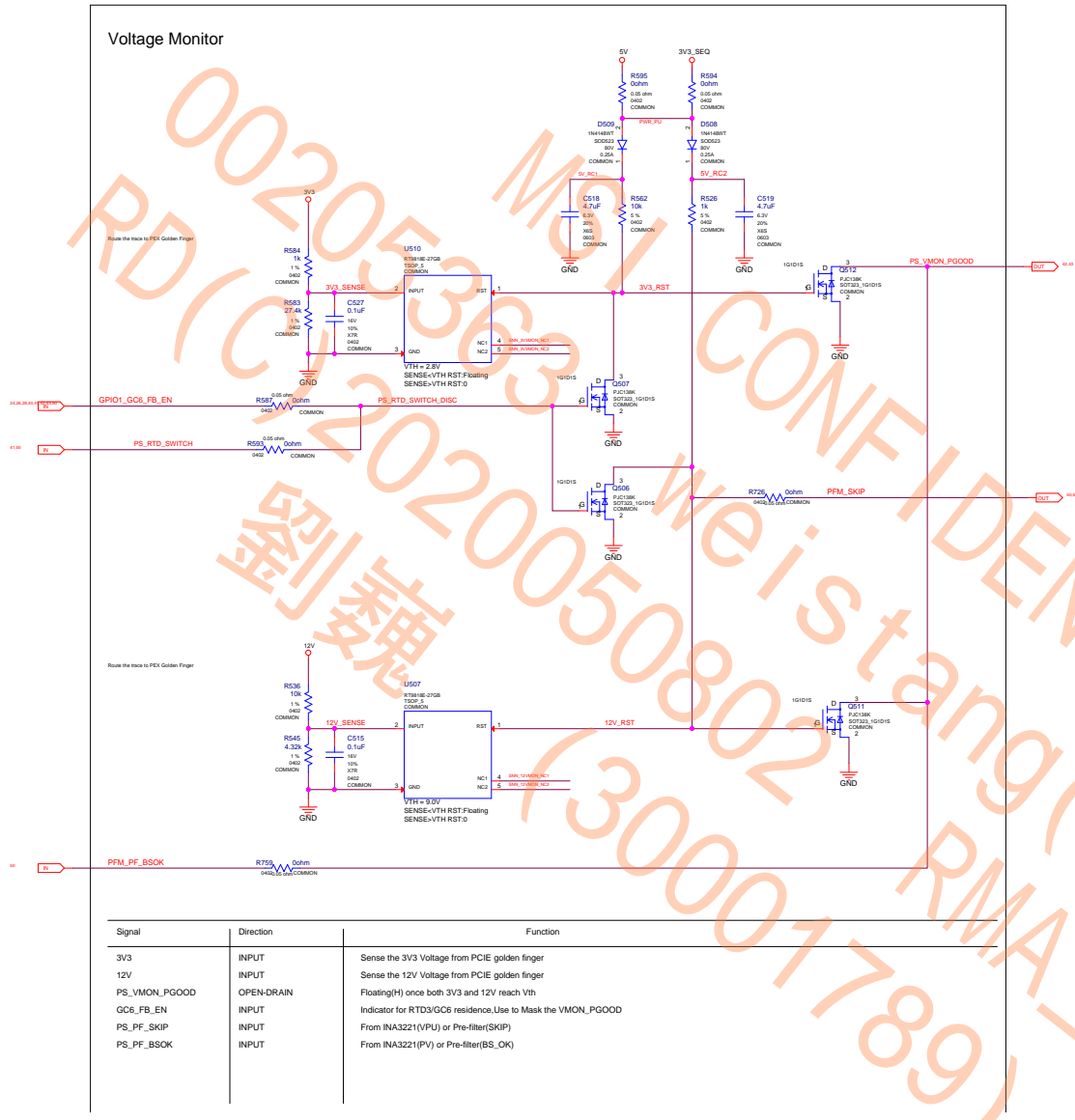


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BOM Configuration

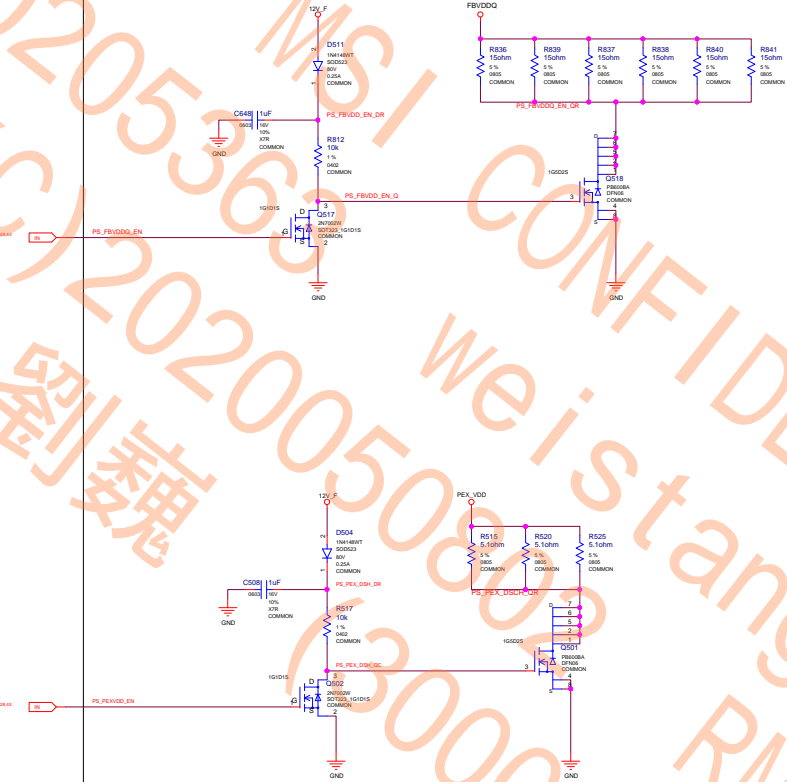
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Use Pre-Filter	Pre-Filter	Pre-Filter	Pre-Filter
Use INA3221	Voltage_Monitor	INA3221	INA3221
NO INA3221 NO Pre-Filter	Voltage_Monitor	Voltage_Monitor	N/A

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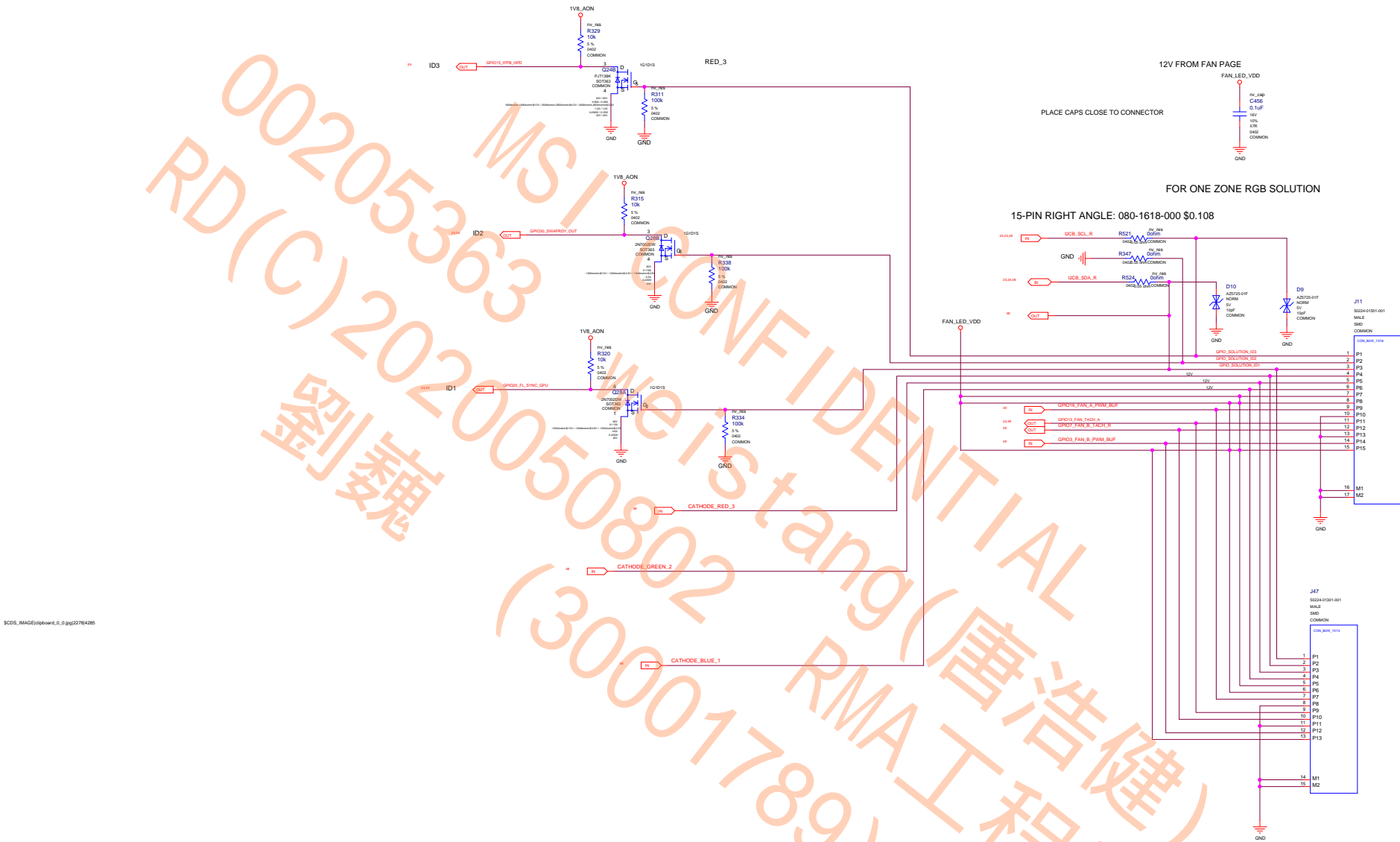
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PCB REV	PG160-802	PAGE	45 OF 52
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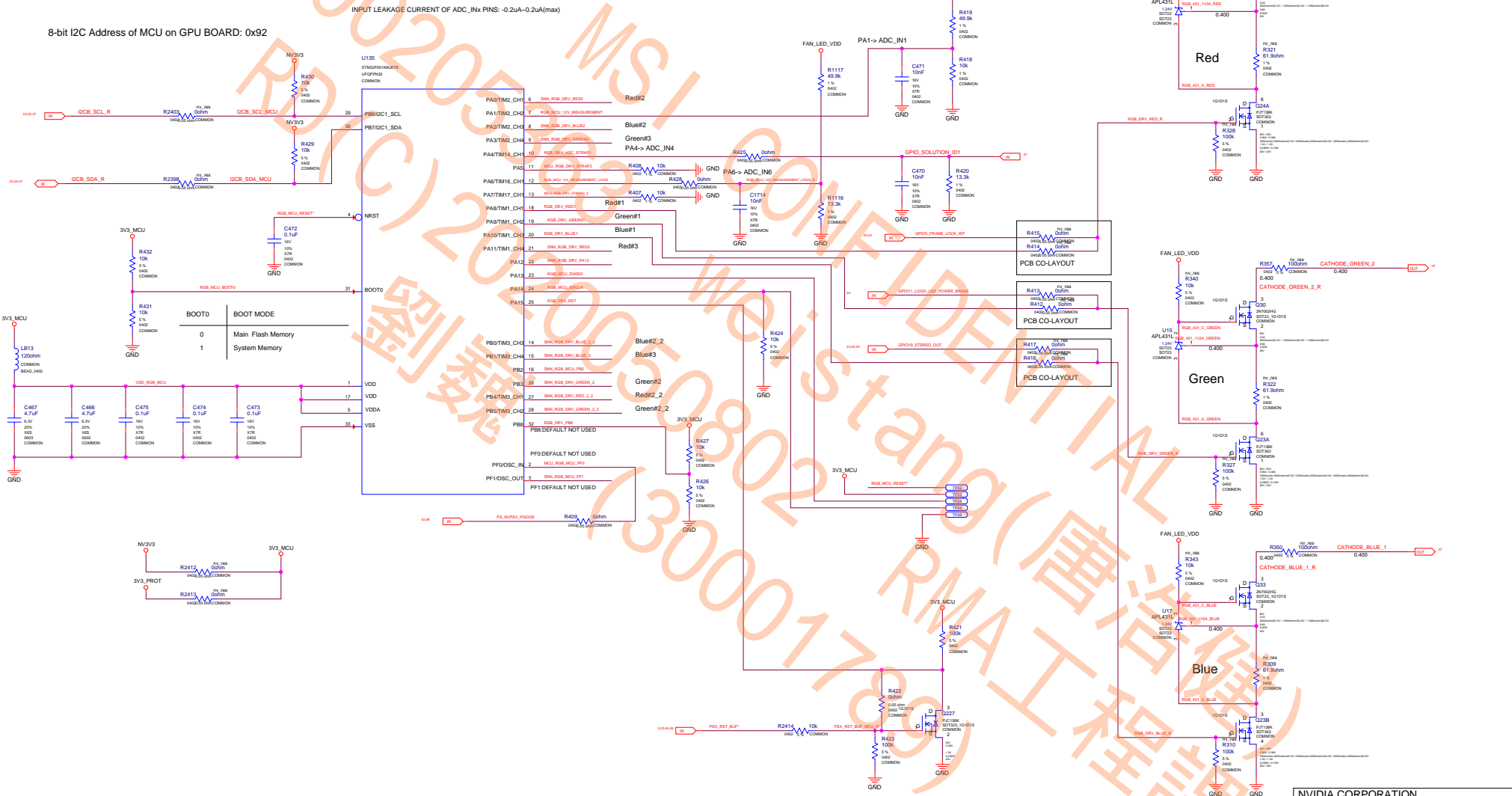
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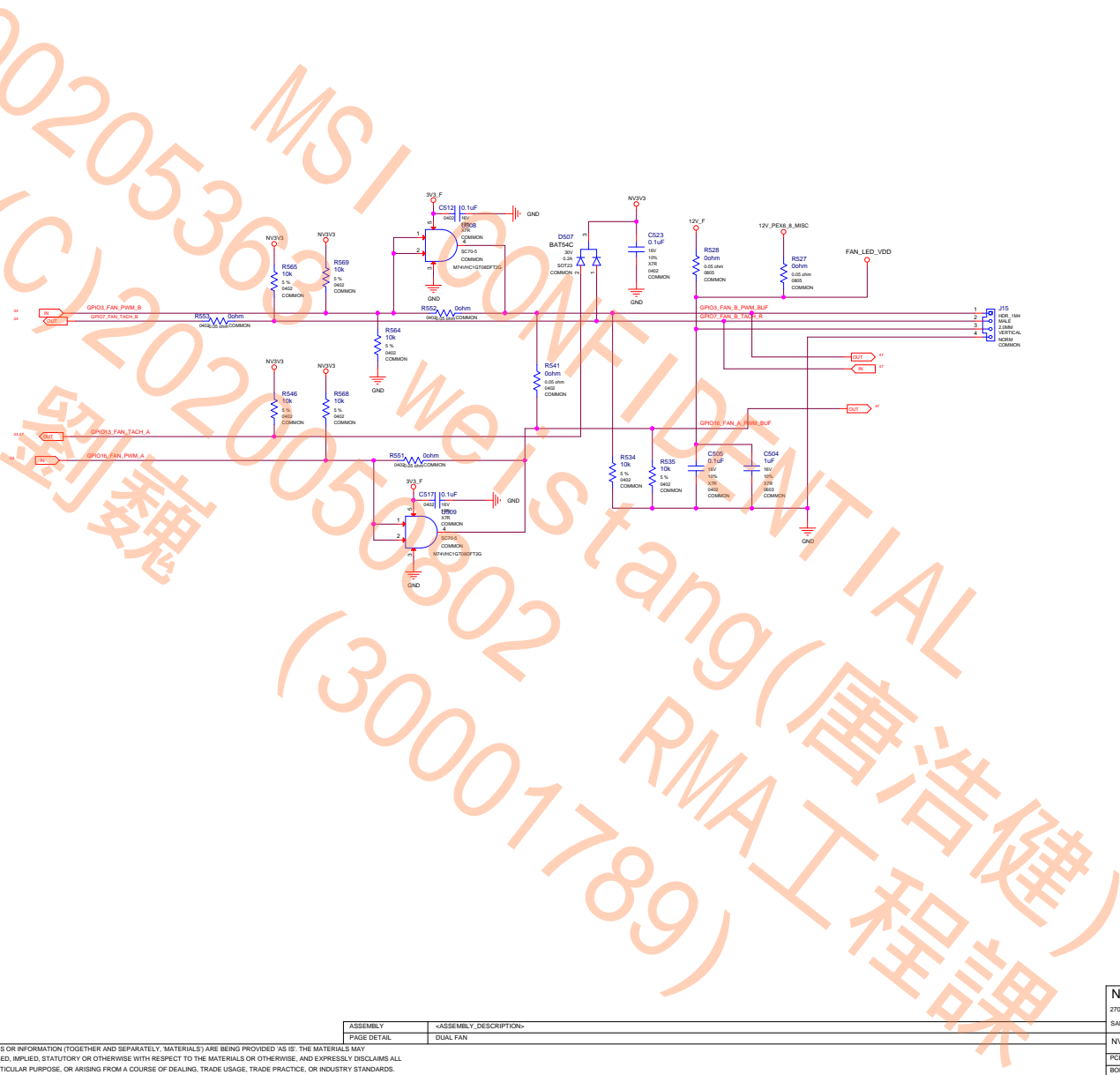


ASSEMBLY	«ASSEMBLY_DESCRIPTION»
PAGE DETAIL	LED DRIVER & BOOST

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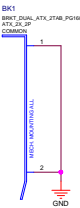


ENABLE PIN STATUS	MULTIPLEXER OPERATION
LOW	FULLY FUNCTIONAL
TRI-STATE(FLOATING)	LIMITED FUNCTION
HIGH	STANDBY

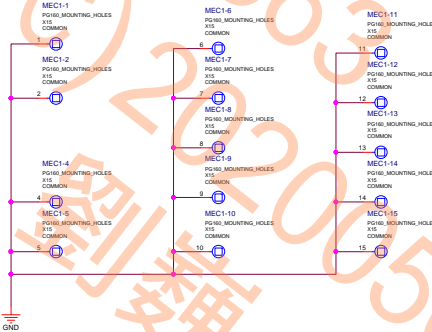
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PCB REV	PG160-B02
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Brackets:



MOUNTING HOLES FOR HS:



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